From Technologies to Markets

Advanced Packaging Current Trends & Challenges



ADVANCED PACKAGING PLATFORMS – Classification at System Integration Level



ADVANCED PACKAGING MARKET SHARE EVOLUTION 2014-2025



In 2019, the AP market share was 42.6%. Due to strong momentum in AP market driven by mega trends, the share of AP in the total semiconductor market is increasing continuously and will reach almost 50% of the market by 2025. In 2014, AP market share was 38% and there is strong possibility that in 2026, AP market share will exceed that of traditional packaging.





ADVANCED PACKAGING REVENUE FORECAST 2019-2025

Advanced Packaging REVENUE SPLIT by packaging platform



Flip-chip	CAGR ₂₀₁₈₋₂₀₂₅ ~ 5.9%%
Fan-out	CAGR ₂₀₁₈₋₂₀₂₅ ~ 16%
Fan-in WLP	CAGR ₂₀₁₈₋₂₀₂₅ ~ 3.2%
3D Stacking	CAGR ₂₀₁₈₋₂₀₂₅ ~ 21.3%
Embedded Die	CAGR ₂₀₁₈₋₂₀₂₅ ~ 18%

- The Advanced Packaging market was worth ~\$28.8B in 2019. It is expected to grow at ~ 7% CAGR₂₀₁₉₋₂₀₂₅ to reach ~\$42.2B in 2025.
- Highest revenue CAGR expected from 2.5D / 3D stacking IC, ED (in laminate substrate) and Fan-Out,
 21%, 18% and 16%, respectively, as high volume products further penetrate the market: FO in mobile, networking, automotive; 2.5D/3D TSV in AI/ML, HPC, datacenters, CIS, MEMS/sensors; ED in automotive and medical.

- NOTE:
 - Values represent packaging services (assembly and test) and do not include FEOL Si die processing
 - TSV* includes portion of package revenue not included in Flip-chip or fan-in

TECHNOLOGY ROADMAP: FROM NANO-SCALE TO MICRO-SCALE..



Industry is looking into the growing importance of functional roadmap

Advanced Packaging is essential to bridge the scale-gap between die and PCB



ADVANCED PACKAGING 2019 WAFER SPLIT BY MANUFACTURER

TEN players, which includes 2 IDMs (Intel, Samsung), a foundry (TSMC), the top 5 global OSATs (ASE, SPIL, Amkor, PTI, JCET) together with Nepes and Chipbond, process approximately 75% of Advanced Packaging wafers.





NOTE:

- This pie chart represents superpositions of all Advanced Packaging platforms (Fan-in/Fan-out WLP, Flip-chip including 2.5D/3D and embedded die.
- Flip-chip values are entered as total capacity, fan-in, fan-out, 3D stacking and embedded die as total production.
- Flip-chip production values were not available by customer global utilization is at ~85-90% of capacity.

FINANCIAL OVERVIEW FOR TOP 25 OSATS

Revenue in 2019





14

SEMICONDUCTOR SUPPLY CHAIN - 2020



MOBILITY ACROSS SEMICONDUCTOR SUPPLY CHAIN AT VARIOUS LEVELS



OSATS PACKAGING BUSINESS CANNIBALIZATION TREND





Thank you!



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